



NO: 244186US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
SATOSHI INOUE : EXAMINER: PAN, DANIEL H.  
SERIAL NO: 10/687,629 :  
FILED: OCTOBER 20, 2003 : GROUP ART UNIT: 2183  
FOR: PROCESSOR AND :  
SEMICONDUCTOR INTEGRATED  
CIRCUIT

AMENDMENT

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated June 11, 2007, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.